

Serial Number: 08/984,563

Dkt: 303.623US4

Filing Date: December 3, 1997

Title: ASYNCHRONOUSLY-ACCESSIBLE MEMORY DEVICE WITH MODE SELECTION CIRCUITRY FOR BURST OR PIPELINED OPERATION

**IN THE SPECIFICATION**

insert the following after CROSS REFERENCE TO RELATED APPLICATION:  
Please amend the specification as follows:

ycc  
10/11/09**CROSS REFERENCE TO RELATED APPLICATION**ycc  
11/11/09

This application is a divisional of application Serial No. 08/650,719, filed May 20, 1996, which is a Continuation-In-Part of application Serial No. 08/584,600, filed January 1, 1996, now U.S. Patent No. 5,966,724.

The below listed applications, as indicated by serial number and filing date, are all assigned to the assignee of the instant application and were or are co-pending with and related to the instant application: Serial No. 08/370,761, filed December 23, 1994 (now U.S. Patent 5,526,320, issued June 11, 1996); Serial No. 08/386,894, filed February 10, 1995 (now U.S. Patent 5,610,864, issued March 11, 1997); Serial No. 08/386,563, filed February 10, 1995 (now U.S. Patent 5,652,724, issued July 29, 1997); Serial No. 08/457,650, filed June 1, 1995 (~~pending~~ now U.S. Patent 6,804,760, issued October 12, 2004); Serial No. 08/457,651, filed June 1, 1995 (now U.S. Patent 5,675,549, issued October 7, 1997); Serial No. 08/497,354, filed June 30, 1995 (now U.S. Patent 5,598,376, issued January 28, 1997); Serial No. 08/505,576, filed July 20, 1995 (now abandoned); Serial No. 08/553,156, filed November 7, 1995 (now U.S. Patent 5,721,859, issued February 24, 1998); Serial No. 08/506,438, filed July 24, 1995 (now U.S. Patent 5,729,503, issued March 17, 1998); and Serial No. 08/630,279, filed April 11, 1996 (now U.S. Patent 5,661,695, issued August 26, 1997).